





Application No:

GB 0303449.3

Claims searched: 1-28

Examiner:

Nigel Hanley

Date of search: 26 September 2003

Patents Act 1977: Search Report under Section 17

Documents considered to be relevant:

Category	y Relevant to claims	Identity of document and passage or figure of particular relevance		
A		WO 1999/038073 A1	NOKIA - See whole document especially Fig 6c & 8 and Page 31 Line 19-Page 32 Line 19. Note communication between two operating systems running on the same processor using interrupt routines.	
A		EP 1162536 A1	HITACHI - See whole document. Note switching of operating systems which share only the system clock and an interrupt controller.	
A .		EP 1054322 A2	HITACHI - See whole document. Note switching of operating systems using interrupts.	
A,E		US 6604123 B1	LUCENT - See whole document. Note concept of portals between protection domains under a single operating system.	

Categories:

x	Document indicating lack of novelty or inventive step	Α	Document indicating technological background and/or state of the art.
Y	Document indicating lack of inventive step if combined with one or more other documents of same category.	P	Document published on or after the declared priority date but before the filing date of this invention
&	Member of the same patent family	E	Patent document published on or after, but with priority date earlier than, the filing date of this application.

Field of Search:

Search of GB, EP, WO & US patent documents classified in the following areas of the UKCV:

G4A

Worldwide search of patent documents classified in the following areas of the IPC7:

G06F

The following online and other databases have been used in the preparation of this search report:

ONLINE: WPI, EPODOC, JAPIO







Application No:

GB 0226887.8

Examiner:

Michael Powell

Waters

Claims searched:

1 to 41

Date of search:

18 July 2003

Patents Act 1977: Search Report under Section 17

Documents considered to be relevant:

Category	Relevant to claims	Identity of document and passage or figure of particular relevance		
A, E		WO 2003/010638 A1	1 (INFINEON) see abstract and points 1 to 4, pages 4 and 5	
A		US 5390310	(WELLAND) see column 18 line 25 et seq	
A			rospace Computer Security Applications Conference, December 38, Clifton and Fernandez, pages 194-198 "A microprocessor designal multilevel security"	

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& Member of the same patent family

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WPI, EPODOC, PAJ, TXTE, INSPEC, IBM TDB, IEEE Xplore